

A memristor-based third-order oscillator: beyond oscillation

A. Talukdar · A. G. Radwan · K. N. Salama

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Abstract This paper demonstrates the first third-order autonomous linear time variant circuit realization that enhances parametric oscillation through the usage of memristor in conventional oscillators. Although the output has sustained oscillation, the linear features of the conventional oscillators become time dependent. The poles oscillate in nonlinear behavior due to the oscillation of memristor resistance. The mathematical formulas as well as SPICE simulations are introduced for the memristor-based phase shift oscillator showing a great matching.

Keywords Memristor · Linear time variant · Oscillator

Introduction

The three basic circuit elements have recently encountered the missing fourth element—‘memristor’, relating between the flux-linkage (φ) and the charge (q), when introduced experimentally by HP labs (Strukov et al. 2008) using a thin semiconductor film (TiO_2) sandwiched between two metal contacts, after the visionary hypothesis given by Chua (1971). According to Strukov et al. (2008) the

memristive property naturally appears in nanoscale devices and has the potential to replace conventional transistors in memory applications (Snider 2007; Yan et al. 2010; Manem et al. 2010), as well as in cross bar switching applications (Wang et al. 2010; Young et al. 2009).

Parametric oscillator is a well known phenomenon in linear time variant system in control theory which occurs only with externally applied periodical forces as in the case of a child on a swing or quartz oscillator (Komine et al. 2003). Such oscillation does not occur automatically, rather external oscillating inputs are required to pump the system. Previously, the oscillating nature of memristor was analytically modeled under sinusoidal input (Radwan et al. 2010a, b). In our recent work (Talukdar et al. 2010, 2011), the nonlinear dynamics of memristor having oscillating resistance and dynamic poles have been presented for memristor-based Wien oscillator, which requires no external input. This letter tries to generalize those concepts in third-order system with a new insight into realizing parametric oscillation by electrical circuit requiring no input force. Mathematical analyses are carried out showing close agreement with SPICE simulations.

Memristor-based phase shift oscillator

Conventional resistors are replaced (one at a time) by memristor in the phase shift oscillator (Fig. 1). R_M represents the resistance of memristor. The SPICE model proposed by Biolek et al. (2009) is used to simulate the effect of memristor on phase shift oscillator. The model is implemented as a SPICE subcircuit with the following parameters: $(R_{\text{off}}, R_{\text{on}}, D, \mu_v, p) = (16 \text{ k}\Omega, 0.5 \text{ k}\Omega, 10 \text{ nm}, 10^{-14}, 10)$, which are the upper resistance, the lower resistance, the thin film width, the dopant mobility, and the

A. Talukdar · A. G. Radwan · K. N. Salama (✉)
Department of Electrical Engineering, King Abdullah University
of Science and Technology, Thuwal, Saudi Arabia
e-mail: khaled.salama@kaust.edu.sa

A. Talukdar
e-mail: abdul.talukdar@kaust.edu.sa

A. G. Radwan
e-mail: ahmed.radwan@kaust.edu.sa

A. G. Radwan
Applied Engineering Mathematics,
Cairo University, Giza, Egypt

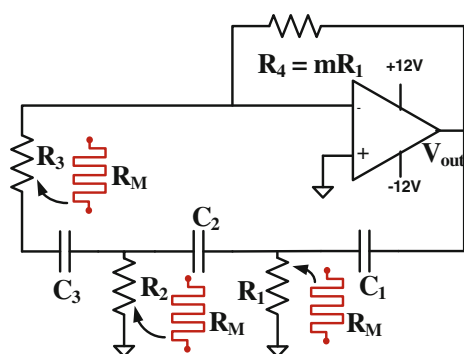


Fig. 1 Schematics of memristor-based phase shift oscillator

nonlinear effect parameter. A parametric study of the initial resistance R_{init} is introduced to validate the concept for different cases.

Oscillating R_M

From the simulation result, sustained oscillation is found even with oscillating behavior of R_M . Though R_{on} (0.5 k Ω) and R_{off} (16 k Ω) are supposed to be the most likely values for R_M , R_M itself shows sustained oscillation with an average value of R_{avg} . Figure 2 shows both the sustained output oscillation and the oscillating R_M (for $R_{init} = 6.5$ k Ω) when R_1 is replaced with R_M keeping R_2 and R_3 as 6.5 k Ω . In this figure it is clearly observed that both R_2 and R_3 are constant at 6.5 k Ω , whereas R_M oscillates within a definite range from 6.05 to 6.95 k Ω . For this third-order-oscillatory system, R_M can be expressed as:

$$R_{max} - R_{min} \cong V_M k (R_{off} - R_{on}) / (\pi R_{init} f_M) \quad (1)$$

where k is given as $k = (\mu_v R_{on}) / D^2$. f_M is the frequency of oscillation and V_M is the voltage across R_M . R_{max} and R_{min} are the maximum and minimum value of R_M . Using (1), the

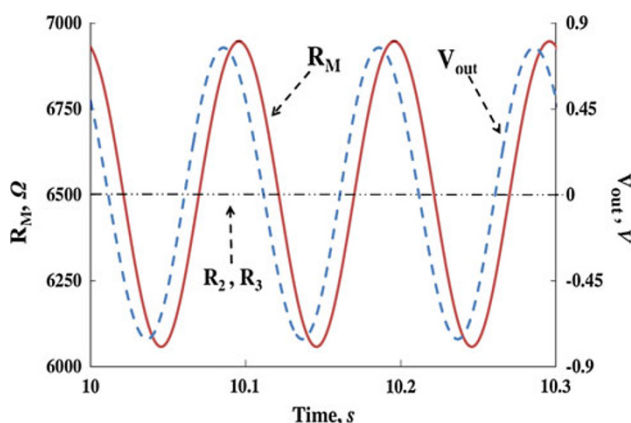


Fig. 2 Simulation results of V_{out} , R_2 , R_3 , and R_M for $R_{init} = 6.5$ k Ω when R_1 is replaced with R_M . Straight line, R_M ; Broken line, V_{out} ; dotted and broken line, R_2 and R_3

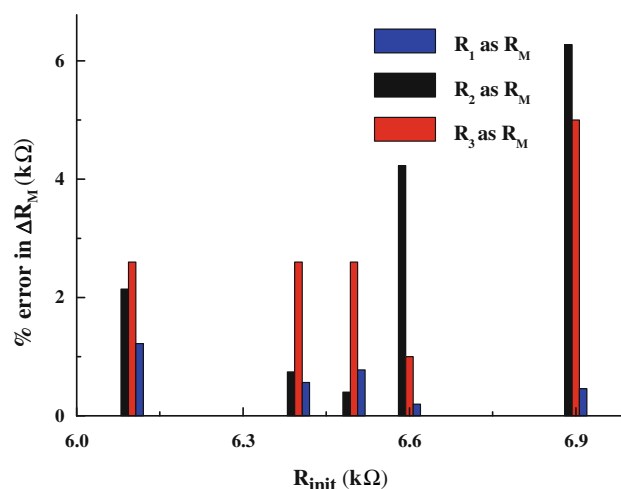


Fig. 3 Percentage error in calculating ΔR_M for different R_{init} when one of the resistors is replaced by memristor

amplitude swing of R_M ($\Delta R_M = R_{max} - R_{min}$) can be estimated for any R_{init} . As R_{init} and R_{avg} are almost equal, ΔR_M will essentially give the values of R_{max} and R_{min} as well. When R_2 or R_3 is replaced with R_M , similar oscillation is found but with different amplitude swing. In case of R_2 , R_M oscillates from 6.28 k Ω to 6.55 k Ω (when $R_{init} = 6.5$ k Ω). For R_3 , the amplitude swing of R_M is observed from 6.37 k Ω to 6.47 k Ω . Figure 3 compares the percentage error in calculating ΔR_M using (1) for different R_{init} . The maximum error in estimating R_M from (1) is found as 1.22% (for R_1 as R_M), for R_2 as R_M the error is 6.3%, and for R_3 as R_M the maximum error is 5%. This oscillating R_M can be the realization of periodically changing parameter. Usually in parametric oscillators, the parameters are externally applied periodically to initiate oscillation but in this memristor-based phase shift oscillator, no external means are required to have the parameter (R_M) oscillating with time.

Oscillating poles

The characteristic equation of this system can be expressed as:

$$as^3 + bs^2 + cs + d = 0 \quad (2)$$

$$a = R_M R^2 C^3 (1 + m), \quad d = 1 \quad (3)$$

$$b = 4R_M R C^2 + 2R^2 C^2, \quad c = 2R_M C + 3RC,$$

where $m = R_4/R_1$ (Fig. 1). There will be three roots which will be defined by the coefficients a , b , c , and d . Two of the roots are complex conjugate and the other one is real. These roots/poles determine the stability of the system, where conventionally it is believed that all the poles will be fixed in the left half of the s plane. But surprisingly in this system poles are found oscillating. From (3), it is obvious that a , b , and c are

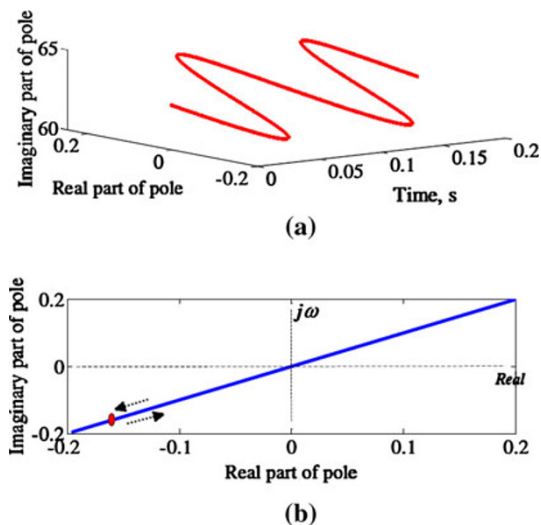


Fig. 4 **a** Oscillating pole with time. **b** s plane representation of oscillating pole

dependent on R_M and as R_M is sinusoidal oscillating, the poles are expected to be dynamic. In Fig. 4a (when R_1 is replaced by R_M), the oscillating behavior of one of the conjugate poles is plotted. Here both the real and imaginary part of complex poles are oscillating with time which means in the s plane that pole will not be fixed, rather it will oscillate from the left half s plane to the right half s plane as shown in Fig. 4b (red circle symbolizes pole). When the pole shifts to the right half plane then the oscillating R_M fetches the pole from the unstable region to the stable region of the left half plane, but then R_M changes its value which tends to pull back the poles towards the right half plane and this periodic incident continues. These oscillations in all three poles are observed for every R_{init} as well as when R_2 and R_3 are also replaced by R_M .

Frequency of oscillation, f_M

Due to the sinusoidally oscillating R_M , f_M can be modeled as:

$$f_M = \frac{1}{2\pi C \sqrt{R(R + 5(R_{avg} \pm \Delta R_M))}} \quad (4)$$

observing (4), f_M will have a range of values from maximum, $f_{M,max}$ (when $R_M = R_{min}$) to minimum, $f_{M,min}$ (when $R_M = R_{max}$). f_M will have a range of frequencies for oscillation but the output oscillation set itself to that frequency, when R_M is equal to R_{avg} . Using (1) R_M can be approximated to be used in (4) to calculate f_M for corresponding R_{init} . The maximum percentage of error in estimating f_M is 0.4% when R_1 is replaced by R_M . In case of R_2 and R_3 replaced by memristor, the maximum error in calculating f_M is 0.45 and 0.1%, correspondingly.

Conclusion

In spite of having oscillating resistance of memristor, and dynamic behavior of poles, sustained oscillation is observed in this memristor-based third-order oscillatory system. For the first time, these unprecedented behaviors of memristor in oscillation can become the circuit implementation of parametric oscillation without any external means. Memristor can be considered as a better candidate than resistor for low-frequency oscillator which is widely used in programmable frequency-based counter, random number generator, synthesizing bass, random number generator, and sophisticated portable system.

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